

CISTER Quicknews

JUNE 2015

CISTER JOINS PEMAS AEROSPACE ASSOCIATION



CISTER has joined the PEMAS association, a broad and multidisciplinary network of Portuguese companies and R&D centres focused on the aerospace industry. The specific objectives of the group are to integrate national and international aerospace industry supply chains; Promote, manage and develop aerospace programmes as a non-commercial entity; and actively contribute to the definition of public policies for the aerospace industry and its involved markets.

THREE SUCCESSFUL PHD DEFENSES

During April three CISTER PhD students concluded their studies and successfully defended their PhD Thesis.



Hossein Fotouhi defended his thesis entitled, “Reliable Mobility Support in Low-power Wireless Networks”, supervised by Prof. Mário Alves. Hossein is now joining the Mälardalen Real-Time Research Centre (MRTC) to work on Embedded Sensor Systems for Health (ESSH) project.

José Marinho defended his thesis entitled, “Real-time Limited Preemptive Scheduling”, supervised by Stefan Petters. In 2013, José’s work granted him the best paper award at RTCSA, the IEEE International Conference on Embedded and Real-Time Computing Systems and Applications.



Borislav Nikolic defended his thesis titled “Many-Core Platforms in the Real-Time Embedded Computing Domain”. He was supervised by Dr. Stefan Peters and the jury was composed of Dr. Petru Eles (Sweden) and Dr. Leandro Soares Indrusiak (Uni. of York, U.K.)

CISTER PARTICIPATES IN TCRTS WORKSHOP ON CMAS

Eduardo Tovar participated in the first Technical Committee on Real-Time Systems (TCRTS) Workshop on Certifiable Multicore Avionics Systems (CMAS).

The goal of the workshop was to bring together the Real-Time Systems (RTS) community to address the challenges in the certification of multicore avionics systems. In particular, contributions on how to analyse and mitigate the effects of interference channels in Commercial-Off-The-Shelf (COTS) multicore processors.



DEWI PROJECT REVIEW MEETING IN BRUSSELS

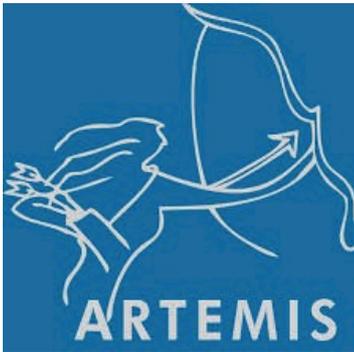
The first year review of the DEWI project took place in ARTEMIS headquarters in Brussels, Belgium. The review meeting was further to the meetings for alignment of requirements in Frankfurt, the technical board meeting in Barcelona and F2F in Alcomendas, Madrid. The preliminary results show that the project was a success regarding the objectives of the first year and the reviewers were pleased with the technical progress of the project. ISEP is leader of SP2 Aeronautics domain which is in charge of two use-cases: the multi-link telemetry logger for rocket launchers, and the active flow control for turbulence reduction on aircrafts. ISEP is also involved in the interoperability domain of the project, which aims to develop a high level architecture for application development that will exploit cross-domain interaction.

FCT/ANI TALK ON H2020 AND ECSEL PERFORMANCE AND OPPORTUNITIES

An information session on H2020 and ECSEL opportunities was organised by FCT at CISTER in April. Representatives from the organisation, João Mil-Homens and Rui Durão, presented the performance of Portuguese organisations in the 2014 calls. They also discussed prospects for the current calls, and the regulations in place for 2015-16.

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CISTER Researchers Luis Lino Ferreira, Geoffrey Nelissen, Eduardo Tovar and Luis Miguel Pinho participated in the 2015 Artemis/ITEA Co-summit held in Berlin.

They were representing CISTER as members of several ongoing and finalized European projects,

CISTER PARTICIPATES IN ARTEMIS CO-SUMMIT

namely: EMC2, CONCERTO, Arrowhead, ENCOURAGE, CarCode and P-SOCRATES.

The Co-summit 2015 organized by ARTEMIS Joint Undertaking on embedded systems and ITEA, the EUREKA Cluster on software-intensive systems and services – counted about 700 participants from industry, academia, public authorities and press from all over Europe. This year's theme was: Smart Industry: Impact of Software Innovation.

During this event, Eduardo Tovar participated in the Artemis General Assembly and the election for the Artemis Steering

Board as a candidate. Luis Miguel Pinho was invited to present a talk in the Speakers Corner on Mixed Criticality related to the actual status of the CISTER-lead European project P-SOCRATES. Geoffrey Nelissen represented CISTER in the CONCERTO booth and Luis Lino Ferreira was also representing CISTER in the CarCode and Arrowhead booths.

Finally, Luis Miguel Pinho and Luis Lino Ferreira, together with the ENCOURAGE project leader Arne Skou, received the achievement award for the successful finalization of the ENCOURAGE project.

P-SOCRATES TECHNICAL MEETING IN ZURICH



The technical leaders of P-SOCRATES organized a meeting in Zurich with all the members of the project international advisory board. On the first day, all the technical leaders presented to the IAB the current status of the project,

the achievements made in each work package, and the perspectives of exploitation for their results.

An afternoon was then dedicated to discuss the strategic position and next moves regarding the dissemination and exploitation of these results. The general assembly took place during the second and third day of the meeting during which the coordinator and all technical leaders prepared themselves for the next project review in May.

ARCS 2015: A HUGE SUCCESS

The 28th GI/ITG International Conference on Architecture of Computing Systems (ARCS 2015) was successfully organized by CISTER, last March.

ARCS has over 30 years of reporting high quality results in computer architecture and operating

systems research. The focus of the 2015 conference was on reconciling parallelism and predictability in mixed-critical systems.

The conference held six sessions featuring 19 papers on hardware, design, applications, trust and privacy and real-time issues.

The conference featured three interesting keynotes: “The evolution of computer architectures: a view from the European Commission”

by Sandro D’Elia of the European Commission Unit, “Architectures for Mixed-Criticality Systems based on Networked Multi-Core Chips” by Roman Obermaisser of the Univ. of Siegen, and “Time Predictability in High-Performance Mixed-Criticality Multicore Systems” by Francisco Cazorla of the Barcelona Supercomputing Center. Four workshops and two tutorials were also part of the conference.

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CISTER - Research Center in
Real-Time & Embedded Computing Systems

